

118

EE318061799US

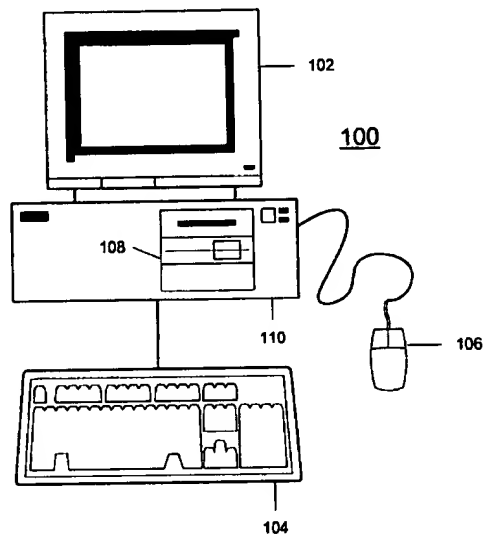


Figure 1
AT9-99-073

09110912-051399



AT9-89-073

318

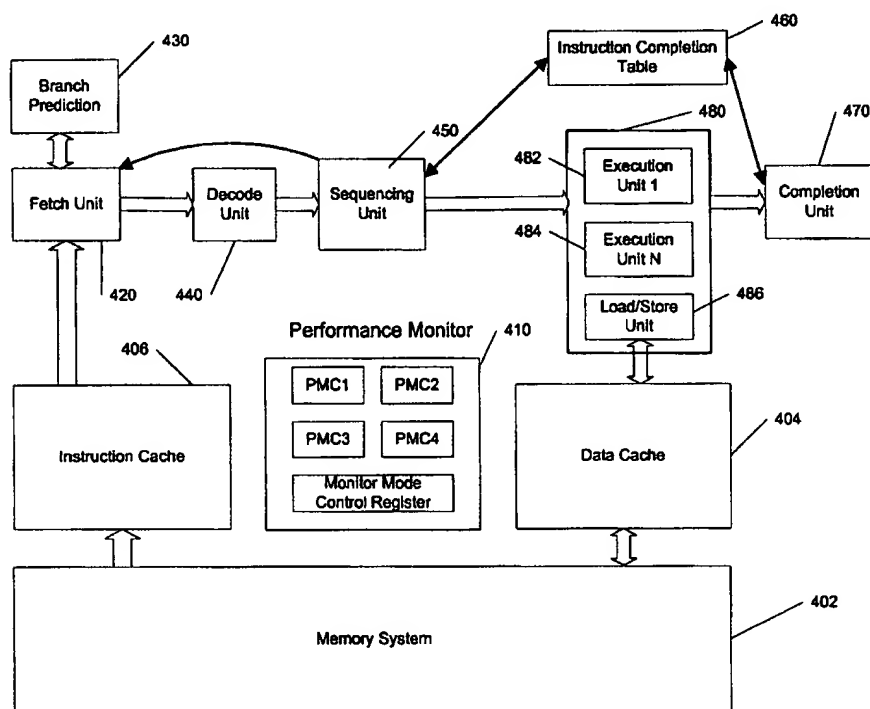
666150"21601660

Bits 0-4 Counting Enables	Bit 5 Interrupt Enable	Bits 6-15	Bit 16 PMC2 Interrupt Control	Bit 17 PMC2 Interrupt Control	Bit 18 PMC2 Count Control	Bits 19-25 PMC1 Event Selection	Bits 26-31 PMC2 Event Selection
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Monitor Mode Control Register
(MMCR)

Figure 3
AT9-99-073

418



400

Figure 4

AT9-99-073

518

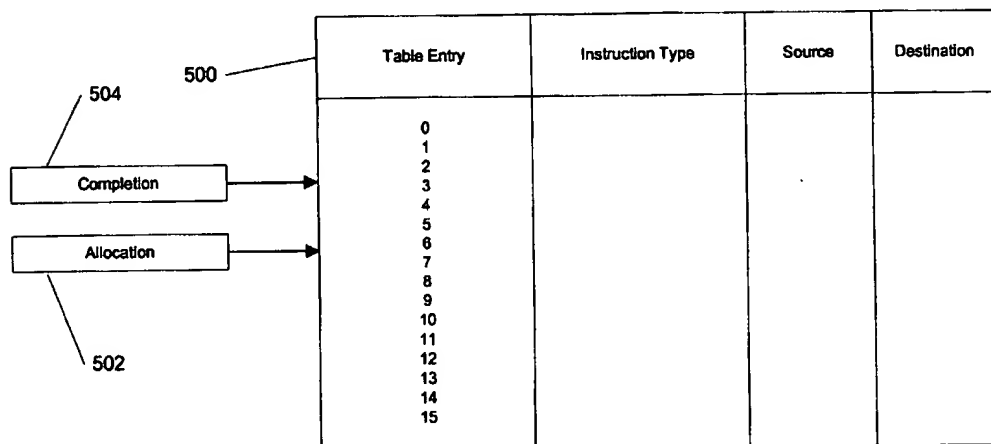


Figure 5

AT9-99-073

618

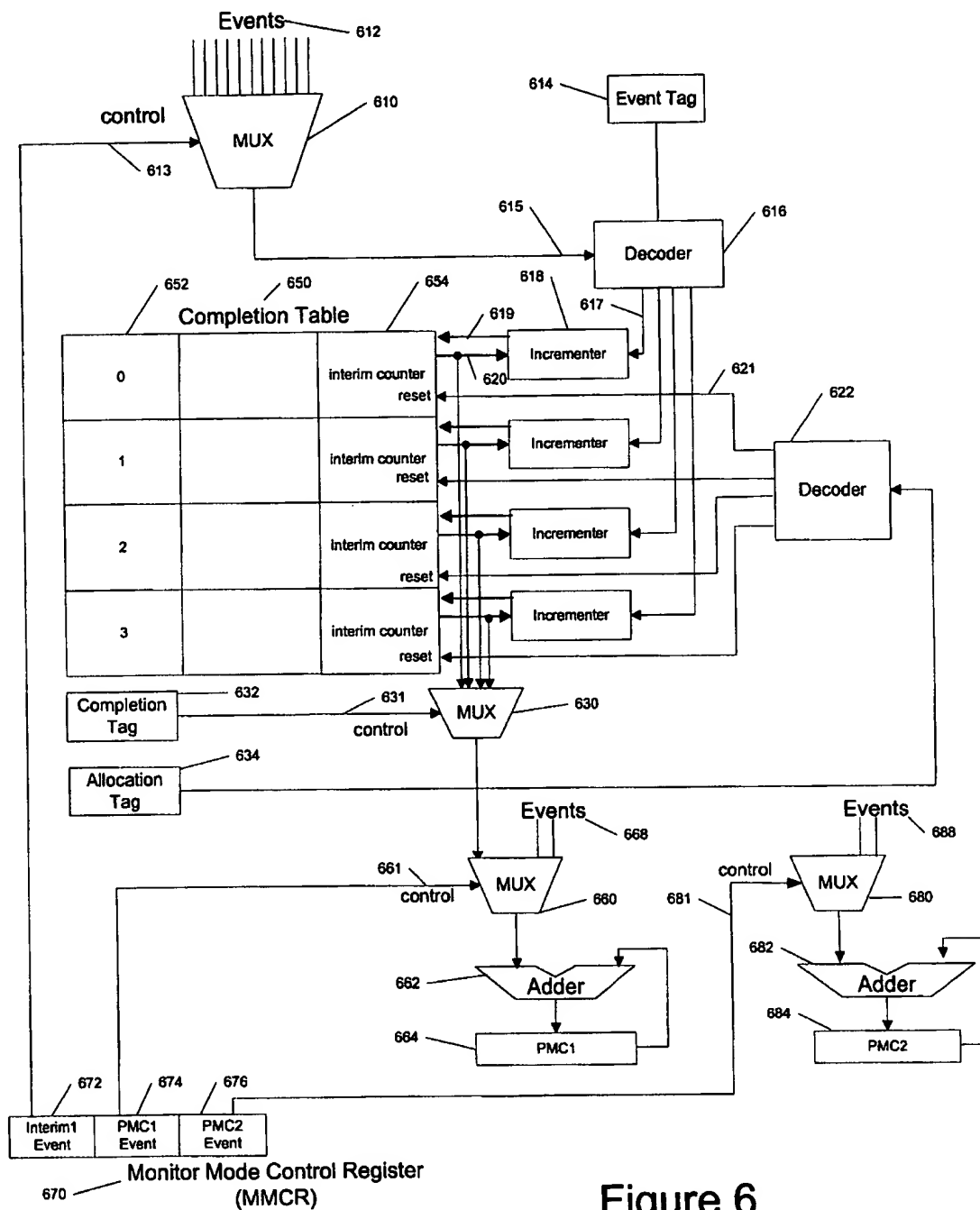


Figure 6

AT9-99-073

718

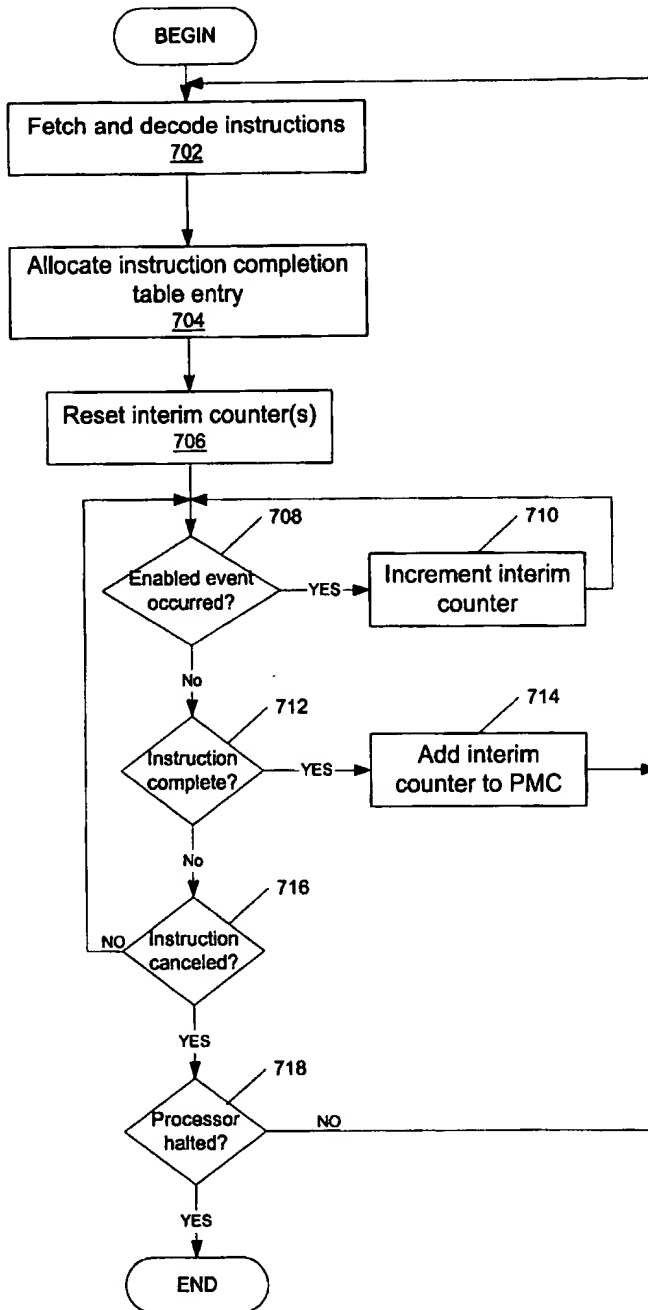


Figure 7
AT9-99-073

00010010-00010010

8/8

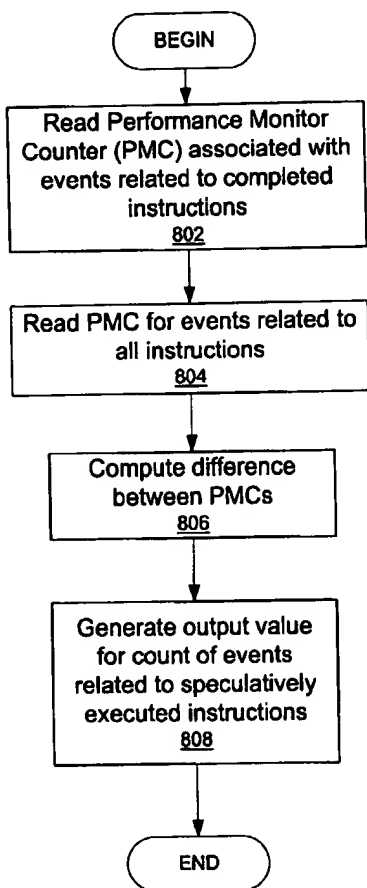


Figure 8

AT9-99-073